



Facsimile

ATTORNEYS AT LAW SINCE 1895

42637

To: Examiner Tran Fax Number: 703 746 4024  
Company: USPTO Phone Number:  
From: Richard J. Coldren Pages: 9  
Date: August 21, 2003 Client/Matter No.: 013721-074563  
I.D. No.: 8715  
Messages:

Re: Attorney Docket No.: DB000575-010  
Serial No.: ~~08/801,628~~

09/801 628

MT 8/19/03

Pittsburgh

Philadelphia

Princeton

Wheeling

Thorpe Reed & Armstrong, LLP  
One Oxford Centre  
301 Grant Street, 14<sup>th</sup> Floor  
Pittsburgh, Pennsylvania 15219-1425  
412 394 7711  
412 394 2555 Fax

**CONFIDENTIALITY NOTICE**

THIS FACSIMILE TRANSMISSION, TOGETHER WITH ANY ATTACHMENTS, IS INTENDED ONLY FOR THE ADDRESSEE. IT MAY CONTAIN INFORMATION WHICH IS LEGALLY PRIVILEGED, CONFIDENTIAL AND EXEMPT FROM DISCLOSURE. IF YOU ARE NOT THE INTENDED RECIPIENT, YOU ARE HEREBY NOTIFIED THAT ANY DISCLOSURE, COPYING, DISTRIBUTION, USE, OR ANY OTHER ACTION OR RELIANCE ON THIS COMMUNICATION IS STRICTLY PROHIBITED. IF YOU HAVE RECEIVED THIS FACSIMILE IN ERROR, PLEASE NOTIFY US IMMEDIATELY BY TELEPHONE TO ARRANGE RETURN OF THE ORIGINAL DOCUMENTS.

**TRANSMISSION PROBLEMS**

If you have any problems with this transmission, please call 412 394 2348.

Received from < > at 8/21/03 11:58:35 AM [Eastern Daylight Time]

08/21/2003 11:50 FAX

The dating stamp of the USPTO on this card 07 June 2001  
will be taken as an indication that the  
accompanying paper(s) was filed.

Applicant(s) Keeth et al. \*transmittal letter x2  
Serial No. 08/801,628 \*PTO/SB/08A  
Filing Date 08 March 2001 \*PTO/SB/08B (2pp)  
Amount of Check N/A \*copies of 33 references  
Atty's File No. DB000575-010  
ELP/CFO/mk

The dating stamp of the USPTO on this card 07 June 2001  
will be taken as an indication that the  
accompanying paper(s) was filed.

Applicant(s) Keeth et al. \*transmittal letter x2  
Serial No. 08/801,628 \*PTO/SB/08A  
Filing Date 08 March 2001 \*PTO/SB/08B (2pp)  
Amount of Check N/A \*copies of 33 references  
Atty's File No. DB000575-010  
ELP/CFO/mk

JUN 18 2001



Received from < > at 8/21/03 11:58:35 AM [Eastern Daylight Time]

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to:

Assistant Commissioner for Patents  
Washington, D.C. 20231



Date: 7 June 2001

**PATENT**

**Attorney Docket No.: DB000575-010**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Applicant(s):</b> Keeth, et al.	)	<b>Examiner:</b> Not yet assigned
<b>Serial No.:</b> 08/801,628	)	
<b>Filed:</b> 08 March 2001	)	<b>Art Unit:</b> 2818
<b>Entitled:</b> 256 MEG DYNAMIC RANDOM ACCESS MEMORY	)	

**TRANSMITTAL FOR INFORMATION DISCLOSURE STATEMENT**

**To:** Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

In regards to the above-referenced patent application, transmitted herewith for filing are:

1. an Information Disclosure Statement By Applicant (Form PTO/SB/08A);
2. an Information Disclosure Statement By Applicant (Form PTO/SB/08B) (two pages); and
3. a copy of each of the thirty-three (33) references listed on Forms PTO/SB/08A and PTO/SB/08B.

Also enclosed is a return postcard. Please date stamp the postcard and return it to us to acknowledge receipt of this information disclosure statement.

The Commissioner is hereby authorized to charge any underpayment or credit any overpayment to our Deposit Account No. 20-0888. A duplicate copy of this Transmittal letter is enclosed.

Respectfully submitted,




Edward L. Pencoske  
Reg. No. 29,688  
THORP REED & ARMSTRONG, LLP  
One Oxford Centre  
301 Grant Street, 14<sup>th</sup> Floor  
Pittsburgh, PA 15219-1425  
(412) 394-7789

Dated: 17 June 2001

Attorney for Applicants



Please type a plus sign (+) inside this box → 

PTO/SB/08B (08-00)

Approved for use through 10/31/2002. OMB 0851-0031  
U. S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449B/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	08/801,628
		Filing Date	08 March 2001
		First Named Inventor	Keeth et al.
		Group Art Unit	2818
		Examiner Name	Not yet assigned
		Attorney Docket Number	DB000575-010
Sheet	2	of	3

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		SUGIBAYASHI ET AL., "A 30-ns 256-Mb DRAM with a Multidivided Array Structure", IEEE Journal of Solid-State Circuits, Vol. 28 (No. 11), (November 1993)	
		TAGUCHI ET AL., "A 40-ns 64-Mb DRAM with 64-b Parallel Data Bus Architecture", IEEE Journal of Solid-State Circuits, Vol. 26 (No. 11), (November 1991)	
		KITSUKAWA ET AL., "256-Mb DRAM Circuit Technologies for File Applications", IEEE Journal of Solid-State Circuits, Vol. 28 (No. 11), (November 1993)	
		JEDEC SOLID STATE PRODUCTS ENGINEERING COUNCIL, "Committee Letter Ballot", JC-42.3-95-73, Item #633.13, Arlington, VA (April 1995)	
		YOO ET AL., "SP 23.6: A 32-Bank 1Gb DRAM with 1 GB/s Bandwidth", ISSCC96/Session 23/DRAM/PAPER SP 23.6	
		NITTA ET AL., "SP 23.6: A 1.6GB/s Data-Rate 1Gb Synchronous DRAM with Hierarchical Square-Shaped Memory Block and Distributed Bank Architecture", ISSCC96/Session 23/DRAM PAPER SP 23.5	
		U.S. Patent Application S.N. 08/521,563, entitled IMPROVED VOLTAGE REGULATOR CIRCUIT, filed 8/30/95	
		U.S. Patent Application S.N. 08/668,347, entitled DIFFERENTIAL VOLTAGE REGULATOR, filed 6/26/96	
		U.S. Patent Application S.N. 08/460,234, entitled SINGLE DEPOSITION LAYER METAL DYNAMIC RANDOM ACCESS MEMORY, filed 8/17/95	
		U.S. Patent Application S.N. 08/420,943, entitled DYNAMIC RANDOM-ACCESS MEMORY, filed 6/4/95	
		U.S. Patent Application S.N. 08/194,184, entitled INTEGRATED CIRCUIT POWER SUPPLY HAVING PIECEWISE LINEARITY, filed 2/8/94	

Examiner Signature		Date Considered	
--------------------	--	-----------------	--

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U. S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

Please type a plus sign (+) inside this box → ☐

Approved for use through 10/31/2002. OMB 0651-0031  
 U. S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE  
 Under the Paperwork Reduction Act of 1995, persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449B/PTO				<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				Application Number	08/801,628
				Filing Date	03/08/2001
				First Named Inventor	Keeth et al.
				Group Art Unit	2818
				Examiner Name	Not yet assigned
Sheet	3	of	3	Attorney Docket Number	DB000575-010

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		U.S. Patent Application S.N. 08/137,679, entitled A VOLTAGE REFERENCE CIRCUIT WITH COMMON GATE LOADING FOR A CURRENT MIRROR OUTPUT STAGE, filed 10/14/93	
		U.S. Patent Application S.N. 08/325,766, entitled AN EFFICIENT METHOD FOR OBTAINING USABLE PARTS FROM A PARTIALLY GOOD MEMORY INTEGRATED CIRCUIT, filed 10/19/94	

Examiner Signature		Date Considered	
--------------------	--	-----------------	--

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U. S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.